EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6	((prevent protect) near2 (reverse adj conduct$4)) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:17
L2	2	((prevent protect) near2 (reverse adj conduct$4)) and comparator and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:18
L3	0	((prevent protect) near2 (reverse adj conduct$4)) and comparator and (second adj (transistor mos mosfet cmos)) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:19
L4	3474	(integrated adj circuit) with (drive driver (power adj (source supply))) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:19
L5	399	((integrated adj circuit) with (drive driver (power adj (source supply)))).ti. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:20
L6	11	((integrated adj circuit) with (drive driver (power adj (source supply)))).ti. and (((high higher) adj voltage) or high-voltage) same (((low lower) adj voltage) or low-voltage) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:21
L7	1	((two adj stage) or (dual adj stage) or two-stage or dual-stage) adj driver and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:22

EAST Search History

L8	0	((two adj stage) or (dual adj stage) or two-stage or dual-stage) adj driver and (second adj (transistor mosfet fet)) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:22
L9	0	(second adj voltage adj rail) with driver with (integrated adj circuit) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:22
L10	12	307/43.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:24
L11	0	307/43,51,75.ccls. and ((reverse adj (current conduct conduct$4)) with (prevent protect prevent$4 protect$4)) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:24
L12	3	307/51.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:25
L13	2	307/71.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:25
L14	9	307/80.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:26

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L15	2	307/85.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:26
L16	4	307/87.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:26
L17	3	307/87.ccls. and (transistor mos mosfet) and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:27
L18	1	307/87.ccls. and (transistor mos mosfet) and comparator and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:27
L19	2	361/82.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:27
L20	3	361/84.ccls. and @pd>="20060406"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 09:28